



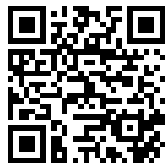
**NITTTR  
BHOPAL**

**EEE-2/2025-2026**

**VLSI Design using VHDL Programming**

**26-05-2025 to 30-05-2025**

**NITTTR Bhopal**



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## Rationale

The electronics industry has achieved a phenomenal growth over the last two or three decades, mainly due to the rapid advances in integration technologies, in short, due to the advent of VLSI design. This program is focused on different stages of VLSI technology, VHDL programming and digital circuit design on Xilinx simulation software so that technical teacher develops skills and help in effective teaching and guiding student for research work.

## Programme Outcomes

The participants of the workshop will be able to:

- Explain Characteristics of CMOS Circuit
- Explain Low power VLSI design.
- Write VHDL programming for VLSI circuit design.
- Use simulation software to design digital Circuits .
- Describe VLSI fabrication and VLSI physical design.

## Programme Content

Overview of VLSI technology, Low power VLSI design, Highlight on VLSI fabrication, VLSI Physical and layout Design, VHDL programming, Design digital Circuits on Xilinx ISE and Application of VLSI.

## Target Group

Faculty of Electronics and allied Engineering disciplines

## Coordinator & Co-Faculty

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